



RISC-V Day
Tokyo 2024 Summer

On August 1st, 2024

BEST POSTER AWARD

AWARDED TO

Masato Isobe, Minoru Watanabe and Nobuya Watanabe

Graduate school of Environmental, life, Natural Science and Technology, Okayama University

FOR THE POSTER

A triple modular redundant RISC-V processor on a Cyclone V

POSTER CHAIR

A handwritten signature in black ink, appearing to read 'H. Nakajo'.

HIRONORI NAKAJO

Institute of Engineering, Tokyo University of Agriculture and Technology