



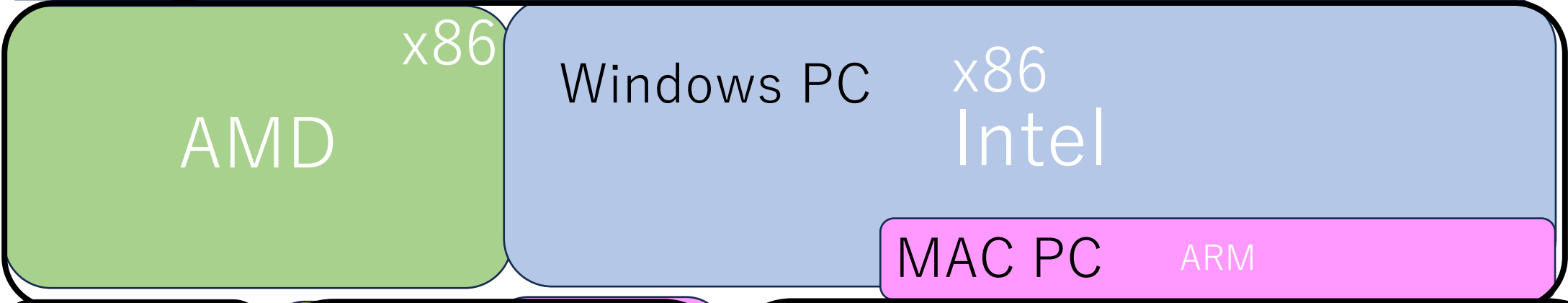
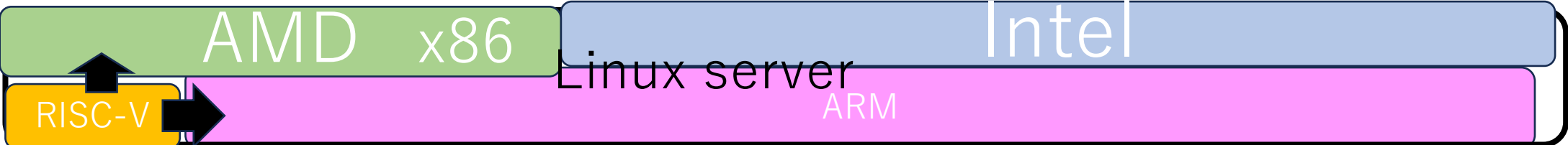
RISC-V day 2025 spring opening

2025.2.27

Hideharu Amano

dlab of the University of Tokyo

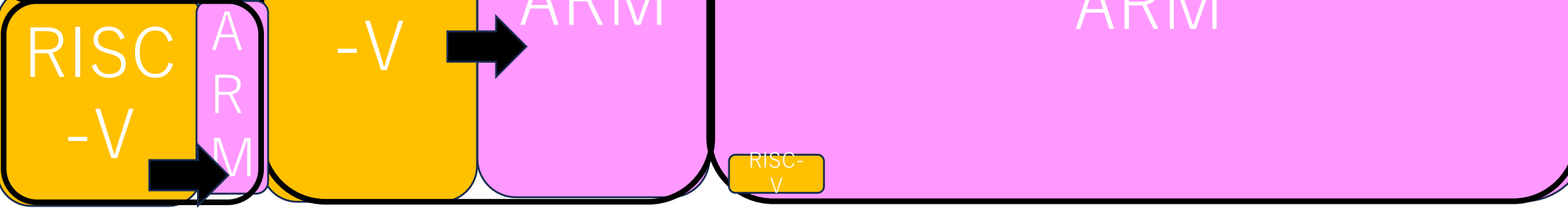
Territory is still small, but growing up!



Host of GPU



Host of AI chip





Today's menu

① RISC-V solution

Main hole 10:00-12:00 14:00-16:00

② Semiconductor Strategy of Japan/ Challenge to GPU

Main hole 16:20-17:20

③ Lunch Meeting

13:00-14:00 Enjoy networking over lunch

④ Technical Exhibition

11:00-16:30 Event space / Foyer

⑤ RISC-V research and Open silicon

13:00-17:15 Special Stage

⑥ Poster Session Record Breaking 22 posters

12:45-13:00 Main hole, 13:00-15:00 Event space/Foyer

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RISC-V TOKYO 2025

SiFive ANDES TECHNOLOGY

MIPS SanKen SANKEN ELECTRIC CO., LTD. VENTANA ARTERIS IP

KMC ISHI JASA RISC-V DAY TOKYO 2025 SPRING DTS インサイト 芯来科技 NUCLEI

TOKYO SPRING
FEBRUARY 27, 2025

2025年2月27日 木曜日 9:00-18:00 東大伊藤謝恩ホール

Here is everything about RISC-V. Enjoy Today!