

Closing Remark

RISC-V Days Vietnam 2020

Prof Pham is tied up with a University Meeting and cannot attend this conference he initiated



[Professor. Cong-Kha Pham](#)
University of Electro-Communications

Part 1

Good afternoon ladies and gentlemen, on behalf of the organizer of RISC-V day Vietnam 18th September 2020, I would like to send to all of you, the speakers, the audiences who attended this online event our warmest greetings.

As you know, “Nvidia is buying British chipmaker ARM from SoftBank for as much as \$40 billion — a total that would make it the largest semiconductor deal ever” is the newest news. This would allow Nvidia, which mainly makes graphics processors for computer games and self- driving cars — to become an industry leader across more connected devices, including smartphones, PCs, robotics and 5G.

Part 2

On the other hand, RISC-V has its conceptual roots in 1980s Berkeley, in part as a direct reaction to the trend towards increasing CPU complexity exemplified by Intel's development of the 8080 via the 8086 into the 80386 during the same epoch. That added instruction set features in silicon as Moore's Law made more transistors affordable; RISC-V went the other way, keeping the core features small and using Moore's Law to speed them up. The obvious advantage over Arm is that RISC-V's instruction set architecture is open source; you can just use it as you wish without paying royalties.

Part 3

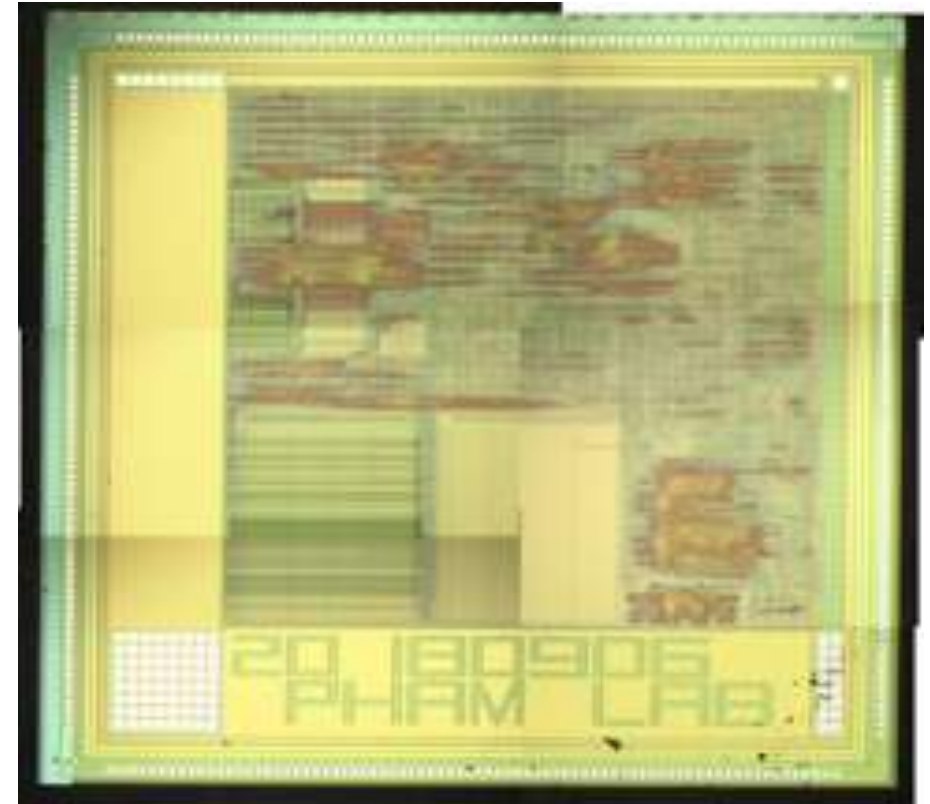
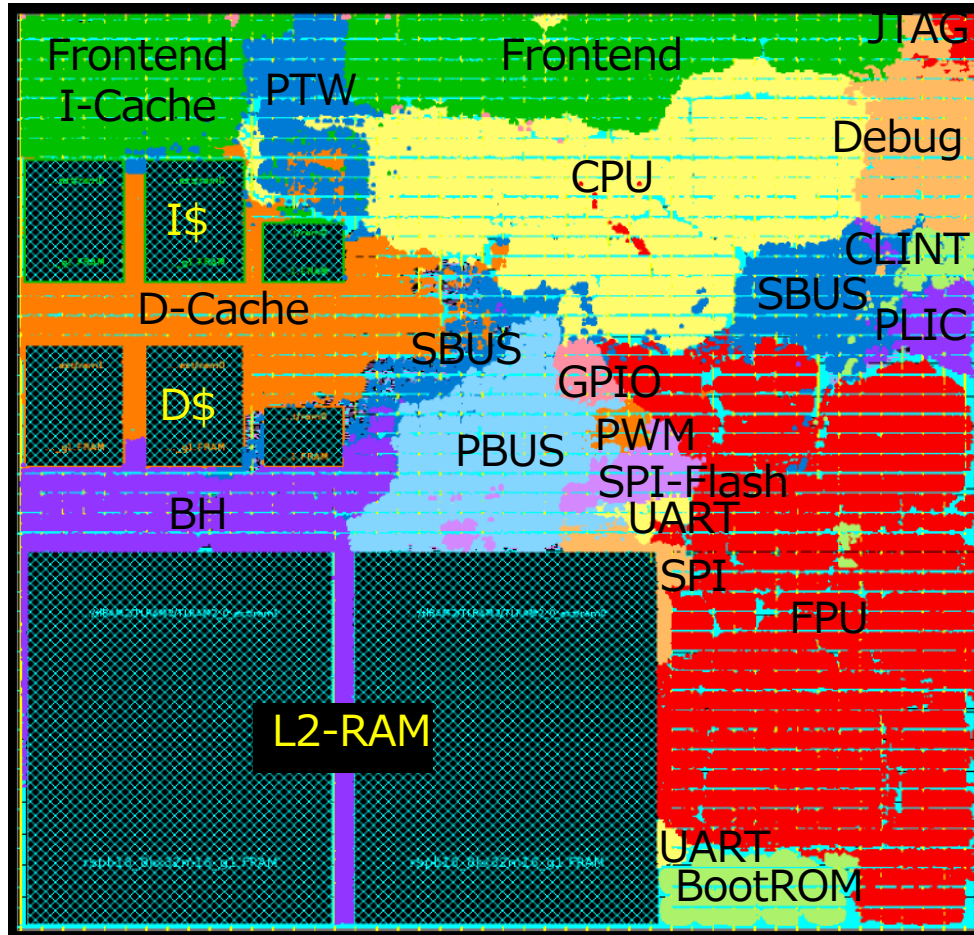
But like open-source software, the fact its free is misleading. You can buy a feature phone with an Arm-based chip in it for a tenner: whatever pennies of that go in CPU licensing don't matter. What RISC-V has that Arm doesn't is extensibility. If you need to add features in the instruction set, go ahead. If you need to tune for very low power or very high throughput, you can. I hope that in the near future, we will make the RISC-V becomes the close competitor of ARM. Last but not least, we would like to thank to all of members who did a lots effort for organizing this online event. Thank you very much, and hope to see all of you in the next event.

An aerial photograph of a university courtyard. The courtyard is paved with a pattern of light and dark grey bricks. Several large, mature trees with vibrant green leaves are scattered throughout the space. There are several wooden benches, some of which have people sitting on them. A few bicycles are parked or being ridden. In the bottom left corner, a set of stairs leads up to a building. The overall atmosphere is bright and sunny, suggesting a pleasant day on campus.

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RISC-V Days Tokyo Live 2020

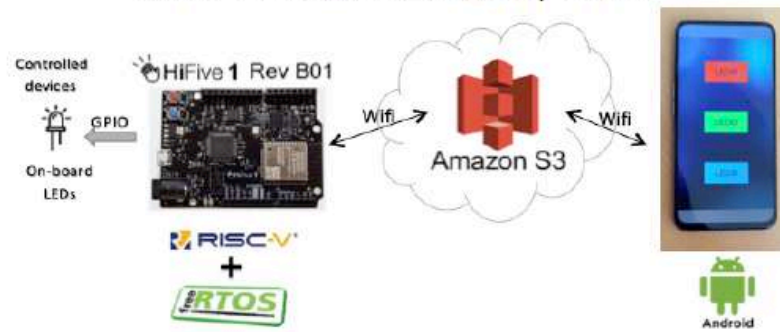
November 5-6, 2020



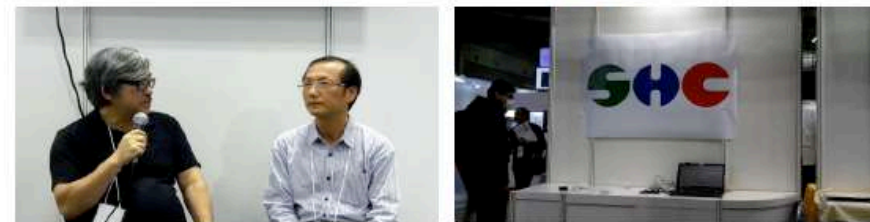
RISC-V Day Tokyo 2019主催 2019年09月30日 (有償登録者 360人)



SHC アマゾン クラウド IoT RISC-V Day デモ
令和元年を記念しRISC-V Dayで開示



ET RISC-V パビリオン出展



Big Thank You!

Cộng đồng vi mạch Việt Nam



Thank you very much!