





AGENDA



Fedora

Fedora 36 on RISC-V



Demo

Run Fedora 36 on QEMU(riscv64)



Distro

Linux Distros on RISC-V



Status

The software component





Part I

Fedora 36 on RISC-V

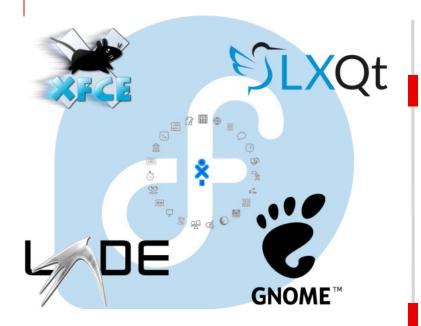








The Status of Fedora on RISC-V





Fedora

Bootable: Yes, OpenSBI + U-Boot on QEMU&Hardware

package management: dnf + rpm

Build system: Koji + Mock

Status: In maintenance, Fedora 36 REPO: 14400+ srpm have been built

Repositories in China

https://isrc.iscas.ac.cn/mirror/fedora-riscv/

http://openkoji.iscas.ac.cn/pub/





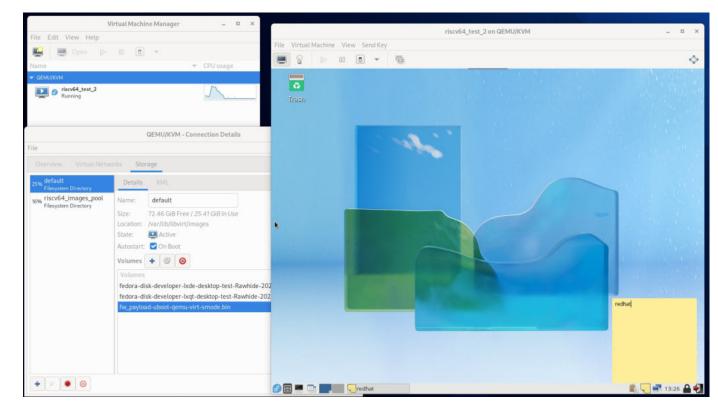
Major test Targets





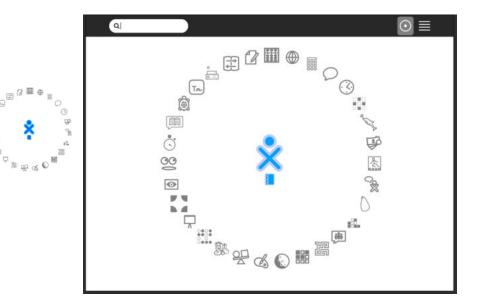
Virtual: QEMU and libvirt/QEMU

Fedora Images can run on the libvirt/QEMU with graphics parameters (Spice).

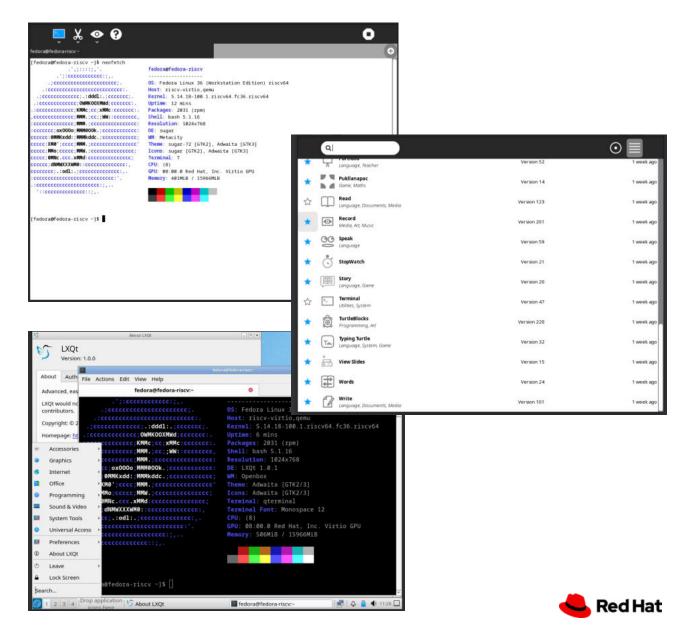




Run Fedora 36 on QEMU(riscv64)



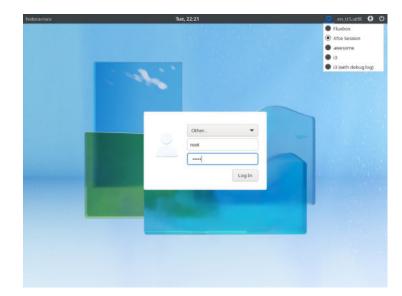


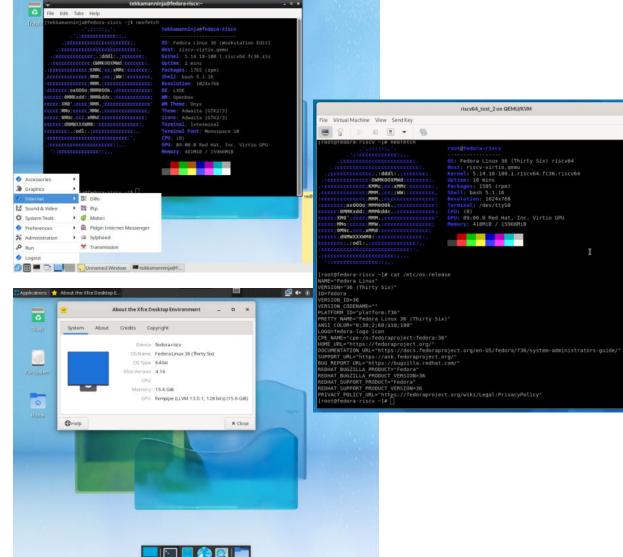




Run Fedora 36 on QEMU(riscv64)











Supported Targets













JingHong Platform - JH71X0

Fedora Images can run on VisionFive.

OpenSBI+U-Boot+Linux kernel are upstreaming.



StarFive VisionFive V1 (JH7100)



StarFive VisionFive V2 (JH7110)











Supported Targets (On Going)



SiFive Unmatched











SiFive Unleashed

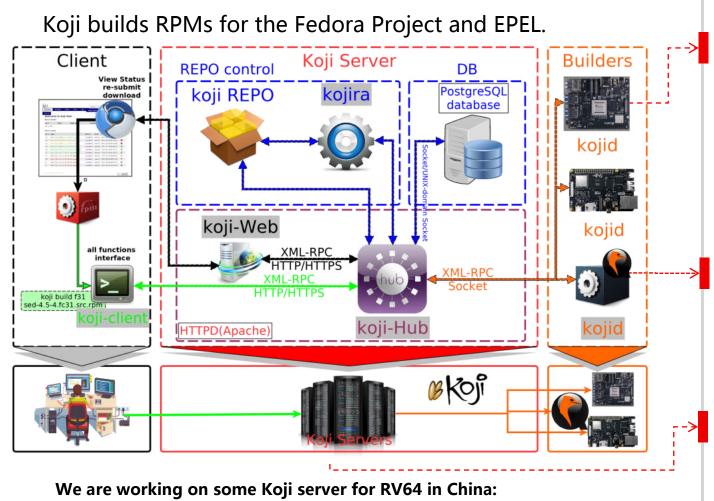




PolarFire SoC Icicle Kit



Koji Build System for RPMs & Image



We MOI

We will have MORE ...

VisionFive 1 & 2



QEMU VMs(on x86_64) 72+



An x86_64 server for all central infrastructure

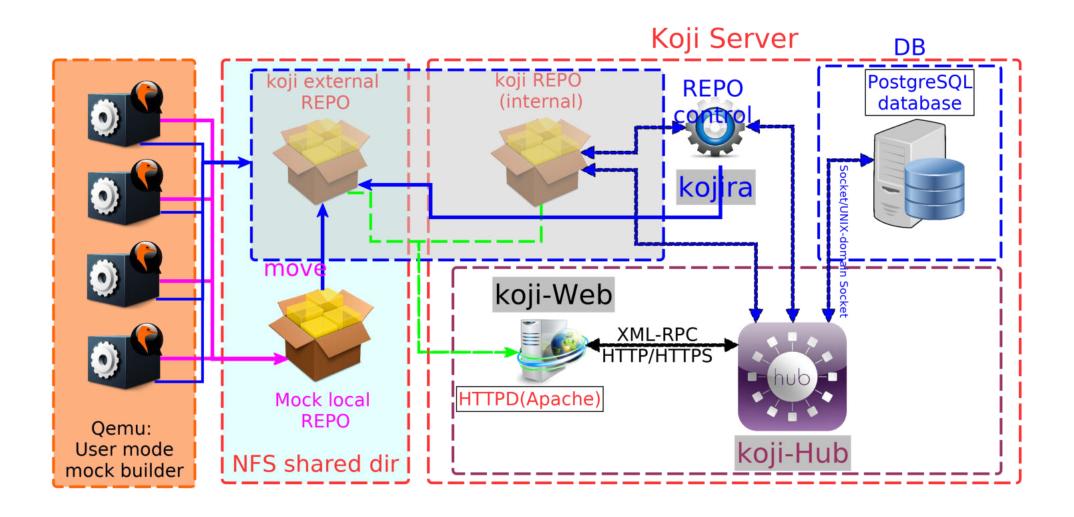
Main sever, repository creation and VMs with backup(separate NVMe).

ISCAS: https://openkoji.iscas.ac.cn/koji/

Oepkgs: https://oepkgs.net/



mock builder(user mode) with Koji Build System





Part II

Linux Distros on RISC-V





Debian

Arch-Linux

Gentoo

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The Status of Linux Distro on RISC-V



Gentoo

Bootable: Yes, OpenSBI + U-Boot on QEMU&hardware

package management: emerge + portage

Build system: portage

Status: **bootable Image**



Chromium OS

Bootable: stage3 rootfs, need to be tested with FW

package management: Build system: cros_sdk

Status: stage3(console), in reproduce, then moving

forward





The Status of Linux Distro on RISC-V



Arch-Linux

Bootable: yes, OpenSBI + U-Boot on QEMU and Hardwares package management: pacman + bsdtar Build system: Arch Build System(ABS), but currently using devtools (systemd-nspawn) Status: **bootable Image**



Info Source:

Arch: Felix Yan(晏然), Sequencer(刘玖阳) Debian: https://wiki.debian.org/RISC-V https://riscv.org/exchange/software/

Debian

Bootable: Yes, on QEMU and Hardware package management: apt + deb

Build system: buildd

Status: In maintenance



The Status of Linux Distro on RISC-V





Info Source:

openEuler: openEuler RISC-V SIG, 中科院软件所

Android: https://github.com/T-head-Semi/aosp-riscv

https://plctlab.github.io/aosp/create-a-minimal-android-system-for-riscv.html

openEuler

Bootable: Yes, OpenSBI + U-Boot on QEMU and Hardwares

package management: dnf + rpm

Build system: OBS, Koji or oepkg

Status: **bootable Image**



Android

Bootable: Yes, OpenSBI + U-Boot on QEMU and C910

package management: apk

Build system: Android Studio

Status: demo can run on C910, ART is underdevelopment



The Status of Embedded Linux on RISC-V





Bootable: No, chroot from Fedora Image package management: slackpkg+pkgtools Status: under development



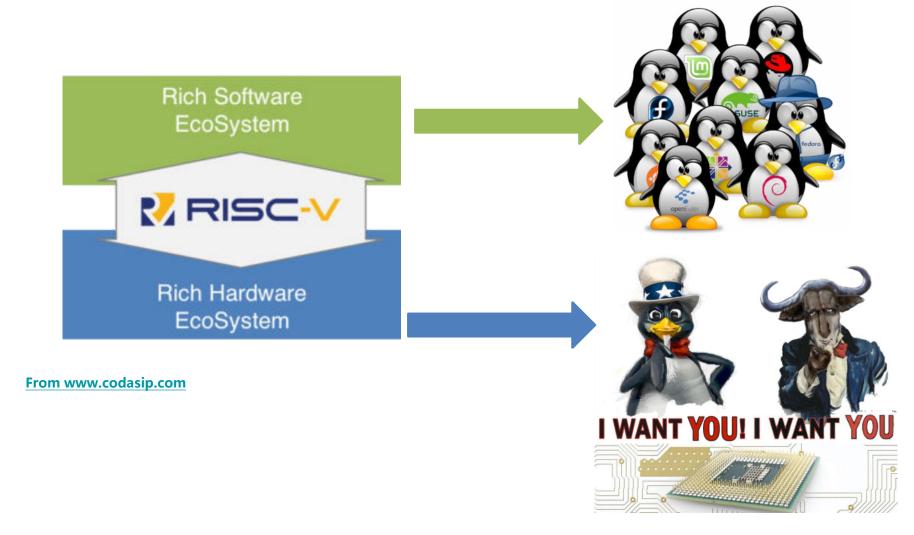
OpenWRT, Buildroot Yocto/OpenEmbedded

Bootable: yes, BBL or U-boot package management: buildtime or Opkg Build system: Cross-compilation

Status: In maintenance



Linux distribution on RISC-V





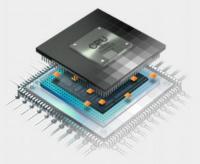
Part III

The software component











The Status of RISC-V Firmware and Linux











OpenSBI

Firmware for RISC-V, upstream main branch, generic platform with the right dtb file.

NO patch required for most of platforms





U-boot

The latest u-boot(upstream, main)with some patches works fine on RISC-V, can boot some Linux distros.



GRUB2



The GRUB(mainline)with a few patches works well on riscv64, can boot Linux distros.



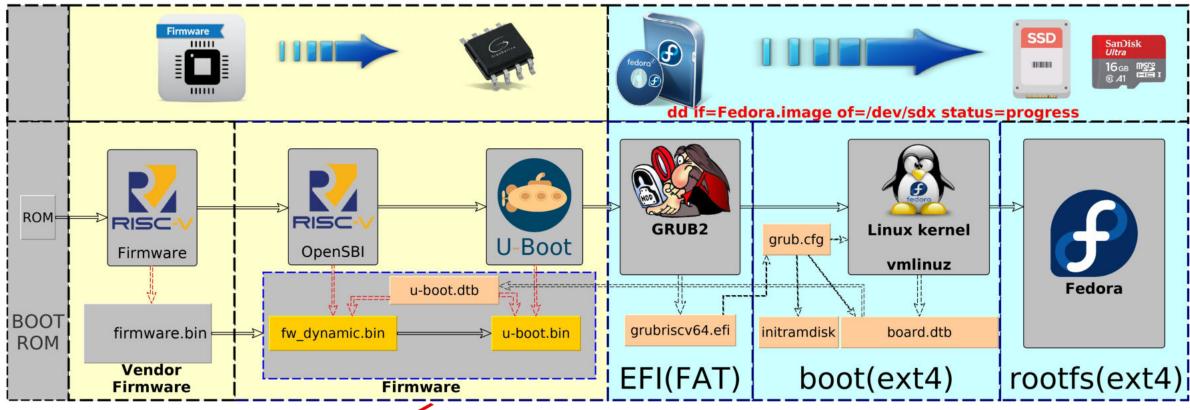
Linux kernel



The **upstream main** branch of Linux works well on RISC-V. We are working with opensource community together on upstreaming the patches for some platform.



Standard boot flow for Linux on riscv64







The Status of RISC-V Firmware for PC & Server







UEFI: Unified Extensible Firmware Interface.

HPE is currently working on the next RISC-V edk2 port release which incorporates with OpenSBI v0.9 that supports the firmware domains for HSM. HPE is also working on RISC-V EDK2 OVMF and Starlight platforms. Contributors from HPE:

Abner Chang Daniel Schaefer

ACPI: Advanced Configuration and Power Interface

Static tables provided by system firmware to the standard ACPI compliant OS for system info and configuration. Contributors from Ventana Micro Systems:

Sunil V L Rahul Pathak Kumar Sankaran Mayuresh Chitale

https://linuxplumbersconf.org/event/11/sessions/114/#20210921



Acknowledgments



Abner Chang Gilbert Chen

Al Stone
Andrea Bolognani
Charles Wei
DJ Delorie
John Feeney
Mark Salter
Richard Jones

David Abdurachmanov

Alistair Francis Anup Patel Atish Kumar Patra

Akira Tsukamoto Drew Fustini Mikael Frykholm Stefan O'Rear















... and countless other individuals and companies, who have contributed to RISC-V specifications and software economics and software economics.







Thank you

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