



# RISC-V DAY

TOKYO 2025 SPRING

TOKYO SPRING

FEBRUARY 27, 2025

## BEST POSTER AWARD

AWARDED TO

Riku Ando

Kawahara Laboratory, Department of Electrical Engineering, Tokyo University of Science

FOR THE POSTER

Efficient Transposed Matrix Multiplication in DNN Training Using Custom RISC-V Vector Extensions

POSTER CHAIR

---

HIRONORI NAKAJO

Institute of Engineering, Tokyo University of Agriculture and Technology