



# RISC-V day 2025 autumn closing

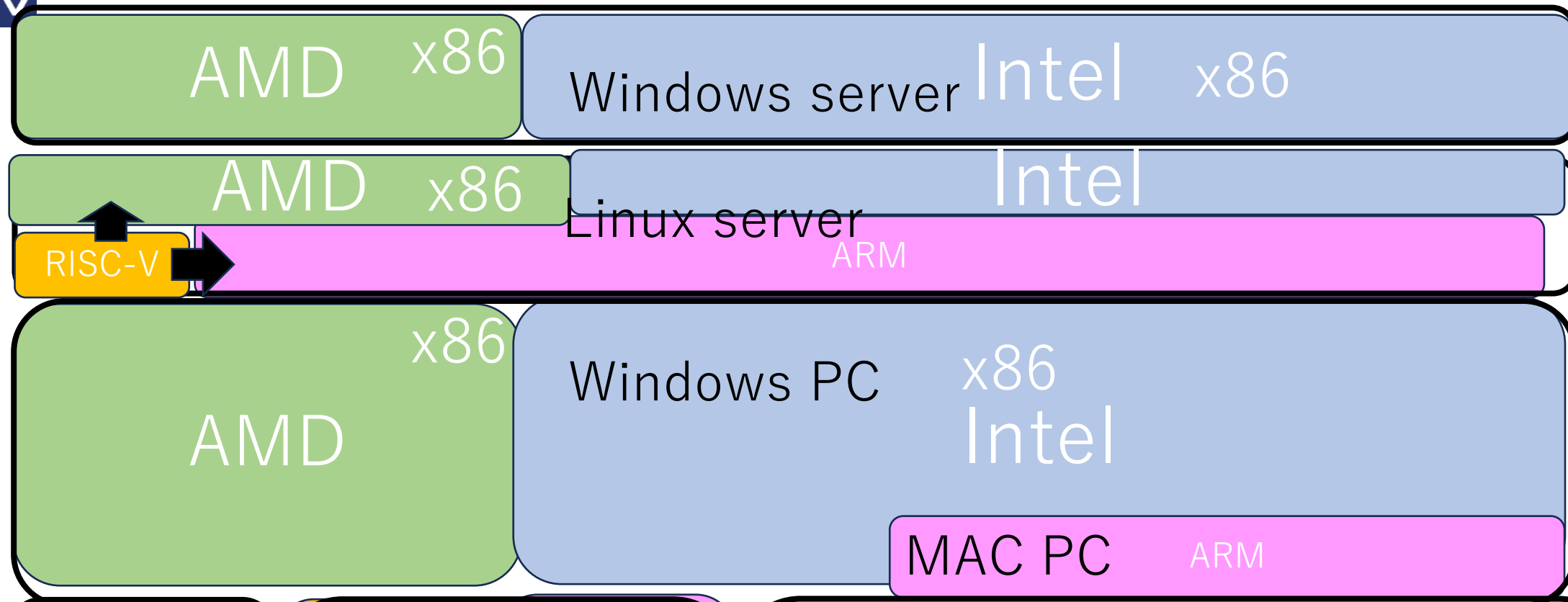


2025.12.4

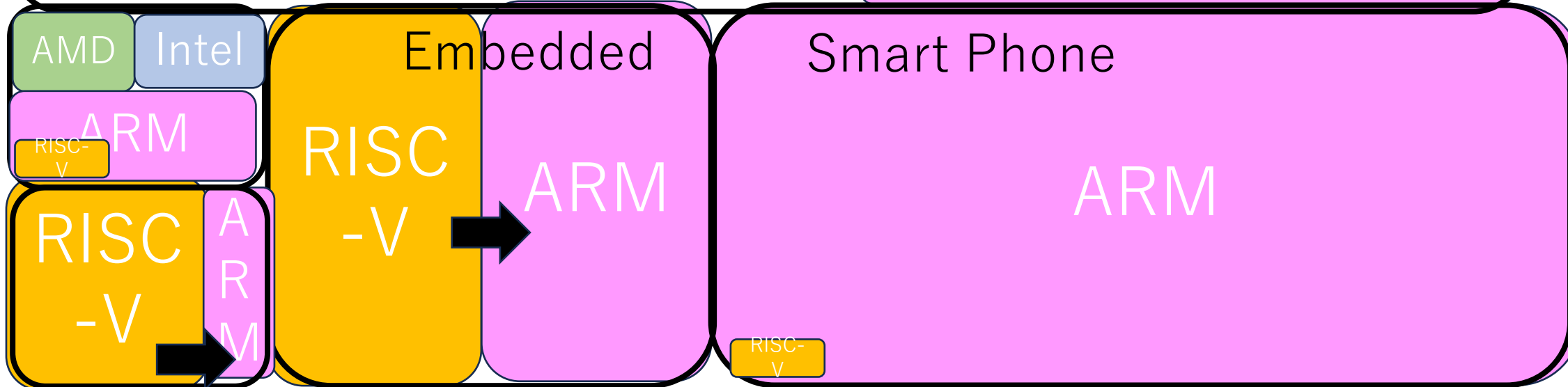
Hideharu Amano

dlab of the University of Tokyo

Territory is growing steadily



Host of GPU



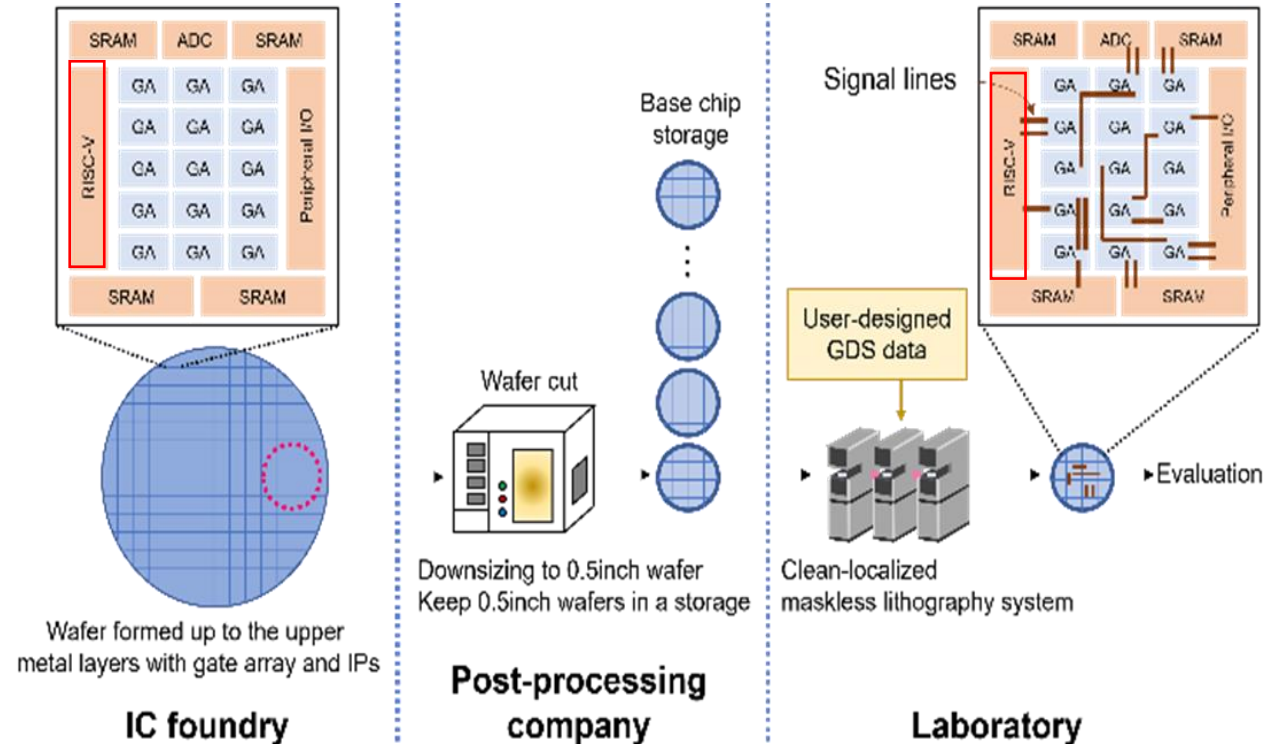
Host of AI chip

- This year's trends:
  - stronger security, with keys kept on the server, and
  - greater freedom, by leveraging the strengths of open source.

## Agile – X Platform

RISC-V + Structured ASIC

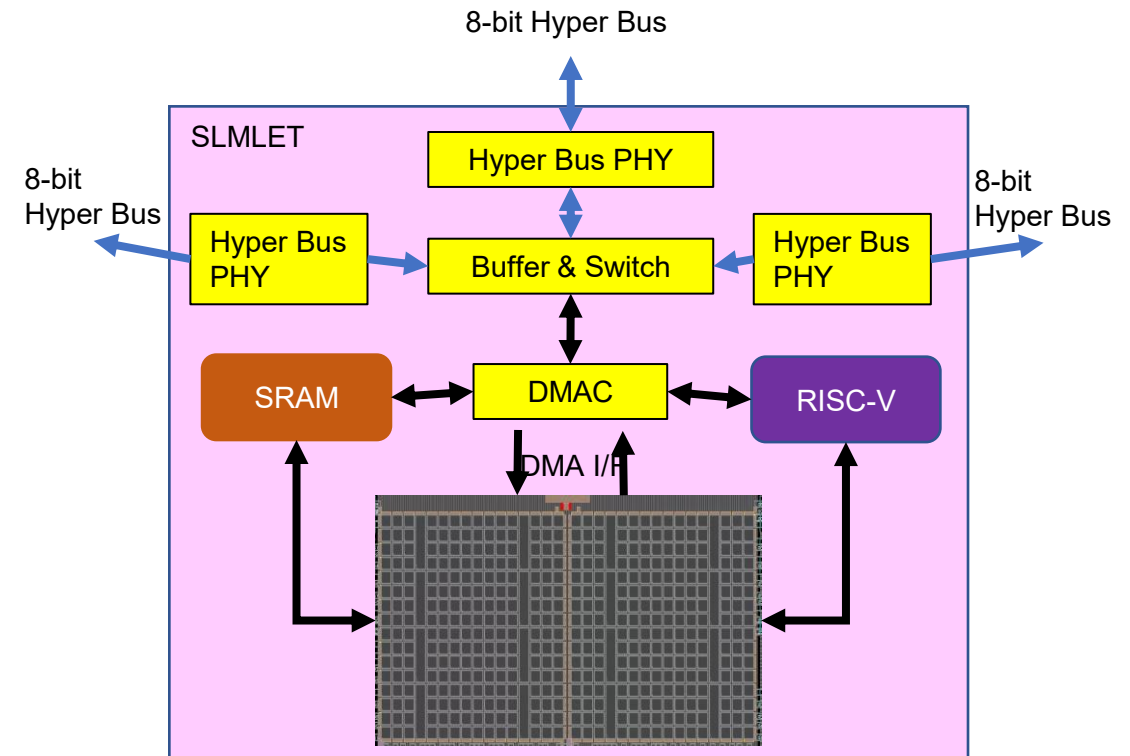
→ Quick and economical SoC



## SLMLET2

RISC-V + Embedded FPGA + Network

→ Expendable IoT device



We will translate it soon.

Hennessy & Patterson & Christos's  
Computer Architecture Textbook 7<sup>th</sup> edition  
based on RISC-V was available 11/7.

See You Again!!

